

J3 Keyboard Connector (between U64 and U65)

Sol-PC, Rev. 2,E
10/18/76

pin no.	Signal name	pin no.	Signal name
1	ground	11	ground
2	+5v	12	+5v
3	Kbd Data Ready	13	Restart
4	Break	14	Local
5	Kbd Data 0	15	KBD Data 4
6	Kbd Data 1	16	KBD Data 5
7	Kbd Data 2	17	KBD Data 6
8	Kbd Data 3	18	KBD Data 7
9	+5v	19	+5v
10	ground	20	ground

J4 Display Expansion Connector (between U28, 29)

pin no.	Signal name	pin no.	Signal name
1	ground	11	ground
2	N.C.	12	N.C.
3	Char. addr. 4	13	Dot Clock, 14.318MHz
4	Character clock	14	Composite sync. out
5	Char. addr. 0	15	TTL Serial Data Out
6	Char. addr. 1	16	Composite blanking out
7	Char. addr. 2	17	Scan advance out
8	Char. addr. 3	18	Char. addr. 5
9	N.C.	19	N.C.
10	ground	20	ground

J5 Personality Module Edge Connector

pin no.	Signal name	pin no.	Signal name
B15	Ground	A15	Ground
B14	+5VDC	A14	+5VDC
B13	Addr. 9	A13	Addr. 0
B12	Addr. 8	A12	Addr. 4
B11	Addr. 7	A11	Addr. 3
B10	INT Bus 0	A10	Addr. 2
B9	INT Bus 1	A9	Addr. 1
B8	INT Bus 2	A8	Addr. 5
B7	INT Bus 3	A7	Addr. 6
B6	INT Bus 4	A6	C4
B5	INT Bus 5	A5	C0
B4	Program 0	A4	INT Bus 6
B3	Program 1	A3	INT Bus 7
B2	Program 2	A2	-12VDC
B1	Program 3	A1	+12VDC

J6 Audio Out for CUPS Cassette Interface: Mini-phone jack at rear panel

J7 Audio In for CUPS Cassette Interface: Mini-phone jack at rear panel

J8 Tape Motor Control 1: (See output port FA, bit 7) Sub-mini jack at rear panel

J9 Tape Motor Control 2: (See output port FA, bit 6) Sub-mini jack at rear panel